Enhancing Flow Boiling Heat Transfer in Microchannels for Thermal Management with Monolithically-Integrated Silicon Nanowires

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Supporting Information

ABSTRACT: Thermal management has become a critical issue for high heat flux electronics and energy systems. Integrated two-phase microchannel liquid-cooling technology has been envisioned as a promising solution, but with great challenges in flow instability. In this work, silicon nanowires were synthesized in situ in parallel silicon microchannel arrays for the first time to suppress the flow instability and to augment flow boiling heat transfer. Significant enhancement in flow boiling heat transfer performance was demonstrated for the nanowire-coated microchannel heat sink, such as an early onset of nucleate boiling, a delayed onset of flow oscillation, suppressed oscillating amplitudes of temperature and pressure drop, and an increased heat transfer coefficient.

KEYWORDS: Si nanowires, microchannels, flow boiling, thermal management

High-flux heat dissipation is becoming the bottleneck to increasing the reliability and enhancing the energy efficiency of many high power electronics and energy systems. Among various thermal management approaches, integrated two-phase microchannel liquid-cooling technology has been envisioned to be a promising solution.1-3 Flow boiling in microchannels can significantly enhance the temperature uniformity in the device substrate along the flow direction as well as reduce the thermal resistance.4-6 However, a major challenge that hinders the implementation of two-phase microchannel heat sinks for practical applications is the severe flow instability.7-9 Over the past two decades, much effort has been made to improve the flow boiling heat transfer performance in microchannels.10-12 For example, it was found that the heat transfer coefficient (HTC) can be increased by creating notches in the sidewalls of the microchannels,13 and nearly steady flow boiling condition was realized by using inlet flow restrictors.14 More recently, microscaled artificial nucleation sites, including micro pin fins15,16 and reentrant cavities17 have been integrated into microchannels, and the heat transfer improvements have been demonstrated. These structured surfaces can mitigate the flow boiling instability induced by the rapid bubble growth at random locations in the microchannels. However, the number and size distribution of the artificial nucleation sites are limited by the microfabrication technology used for fabricating these devices.

Very recently, Chen et al.18 and Li et al.19 showed that nanowires can significantly enhance the pool boiling heat transfer performance. It was found that the nanowire-coated surfaces are superhydrophilic, and the agglomeration of the nanowires gives rise to numerous nucleation cavities with sizes ranging from submicrometer to a few micrometers, which help enhance HTC and delay CHF of pool boiling. Following the same line of reasoning, we expect that the nanowire coatings could similarly enhance the flow boiling performance of microchannel heat sinks if the nanowires can be successfully integrated in microchannels.

In this work, we study the effect of nanowires on the flow boiling heat transfer in a two-phase microchannel heat sink. We present for the first time the fabrication of parallel microchannel arrays with in situ silicon nanowire integration. By comparing the flow boiling characteristics in microchannels with and without nanowires, we demonstrate that the flow boiling performance can be significantly enhanced in the nanowire-coated microchannels with an early onset of nucleate boiling.
boiling (ONB), a delayed onset of flow oscillation (OFO), suppressed oscillating amplitude of temperature and pressure drop, and an augmented HTC.

We fabricated both the plain-surface microchannel arrays and the nanowire-coated microchannel arrays. Figure 1 shows the schematics of the Si nanowire-coated microchannels (a) and the photos of the actual fabricated device (b, c). Both the plain-surface and the nanowire-coated microchannel arrays consist of 14 parallel channels with inlet/outlet plenums, the PDMS cap, and the on-chip heating element and temperature sensors. Each microchannel is 20 mm long, 250 μm wide, and 200 μm deep. The microchannels are equally spaced with a channel wall thickness of 90 μm. The footprint of the microchannel array is 20 mm × 4.67 mm on the silicon substrate, and at the backside a gold thin film heater of the same size is fabricated to provide electrical resistive heating. Three resistance temperature sensors (also made of gold) are fabricated on the heater side to measure the temperatures at the inlet, the outlet and the middle location of the microchannels, \( T_1, T_2, \) and \( T_3 \), respectively. Figure 2 shows the scanning electron microscopy (SEM) images of the Si nanowire-coated microchannels before flow boiling experiments. It is observed that Si nanowires are vertical to the substrate and numerous microvacancies are formed due to the entanglement of these high aspect-ratio nanowires during the fabrication process. The detailed fabrication processes are described in Supporting Information (Figure S1).

Using the similar testing procedure as reported by Liu and Garamella,

we tested the flow boiling characteristics of both the plain-surface microchannels and the nanowire-coated microchannels at different mass fluxes. Schematics of the test setup and the experimental procedure, together with the data reduction and measurement uncertainty, are reported in Supporting Information (Figures S2 and S3). Generally, it was found that at low heat fluxes the wall temperature increases linearly with the heat flux, which indicates the flow is in the single-phase liquid flow regime. With increasing heat flux, vapor bubbles start to form in the microchannels, and the ONB is identified by visual inspection of the first appearance of the vapor bubbles. Following ONB, further increase in the heat flux results in periodic temperature and pressure fluctuations with considerable amplitude. This condition is identified as the OFO, where the vapor phase generated may even surge upstream against the incoming liquid. If we continue to increase the heat flux to a sufficiently high level, a rapid and unsteady temperature rise will be observed at the exit of the microchannels (corresponding to the location of temperature sensor \( T_3 \)). The temperature rise is accompanied by a fast vapor backflow toward the inlet, which could completely block the liquid supply into the microchannel and result in device failure due to overheating. In our experiments, this is recognized as the CHF condition.

Figure 3 compares the flow boiling curves for both the plain-surface microchannels and the nanowire-coated microchannels, where the inlet fluid temperature \( T_{in} \) is fixed at 60 °C and the mass flux \( G \) varies from 119 to 571 kg/m²s. It is noted that because of the axial conduction effect on the wall temperature measurements, \( T_2 \), which is measured at the middle location of the microchannel array (as shown in Figure 1c) is taken as the representative wall temperature. Additional flow boiling curves for \( T_{in} = 30 °C \) at mass fluxes \( G = 119–571 \) kg/m²s can be found in Supporting Information (Figure S5). An early ONB at a lower heat flux is observed in Figure 3 for the nanowire-coated microchannels as compared with that for the plain-surface microchannels. After ONB, a rapid transition to OFO occurs in the plain-surface microchannels, accompanied by periodical temperature and pressure fluctuations. This is due to the rapid bubble growth following ONB at random locations in the microchannels, which limits the heat flux range for stable flow boiling. In contrast, stable flow boiling is observed in the nanowire-coated microchannels over a much larger range of heat flux. Thus, flow instability is suppressed in the nanowire-coated microchannels. Additionally, once the mass flux reaches 238 kg/m²s and above, appreciable heat transfer enhancement can be found in the nanowire-coated microchannels, as evidenced by the reduced wall temperature when comparing with that in the plain-surface microchannels (Figure 3, parts b–d).

The time-dependence of the wall temperature \( T_2 \) is illustrated in Figure 4a for \( G = 571 \) kg/m²s and \( q = 80 \) W/cm². In the nanowire-coated microchannels, the average wall temperature is about 8 °C lower than that in the plain-surface microchannels, and the amplitude of temperature fluctuations also decreases significantly. According to Kandlikar,9 a reduction of the wall temperature is one effective way to suppress flow instability. Figure 4b shows the pressure drop across the microchannels under the same flow conditions. Similarly, the average pressure drop in the nanowire-coated microchannels is

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**Figure 1.** (a) Schematics of the nanowire-coated microchannels and the optical images of the microfabricated device: (b) Top view from the transparent PDMS cap and (c) backside view with gold thin film heater and resistive temperature sensors.

**Figure 2.** SEM images of the Si nanowire-coated microchannels before flow boiling tests. On the right are the cross-sectional and the top views of the nanowire arrays on the microchannel surface. Due to the agglomeration of the nanowires, numerous microvacancies with a size range of a few micrometers are formed, which could serve as the boiling nucleation sites.
decreased by 1.1 psi and the fluctuations are decreased by 1 psi as compared to those in the plain-surface microchannels. Thus, Figure 4 demonstrates that the nanowire coatings indeed help suppress the flow instability in the microchannels.

To understand the heat transfer enhancement mechanisms, we visualized the flow boiling characteristics using a high-speed camera (FASTCAM SA4). Figure 5 shows the two-phase flow patterns in the middle channel of the microchannel array for \( G = 357 \) kg/m\(^2\) s and \( q = 48 \) W/cm\(^2\). The images presented were taken at a location 6 mm downstream to the inlet. The moderate values of mass flux and heat flux were chosen because they produce well-defined flow patterns. The flow patterns in the nanowire-coated microchannels are less distinguishable than those in the plain-surface microchannels, due to the light absorption by the nanowires.\(^{22}\) Hence the outlines of the flow patterns are sketched in Figure 5 to facilitate the comparison.

While Figure 5 only shows the static images, periodic two-phase flow motions were observed when the heat flux increases beyond the OFO, and one cycle can be divided into three
stages: the bubble nucleation stage, the vapor filling stage, and the upstream flooding stage.

(1) During the bubble nucleation stage, sparse vapor bubbles appears primarily at the corner of the plain—surface microchannel where the highest wall superheat is reached (Figure 5a). In contrast, more abundant bubbles are found uniformly distributed in the nanowire-coated microchannel (Figure 5b). The flow boiling enhancement by the nanowire coatings can be explained as following. First, while the nanowire-coated surface exhibits superhydrophilicity with a contact angle of 6° for water, the agglomeration of nanowires gives rise to numerous micrometer-sized cavities ranging from 1 to 15 μm (as shown in Figure 2). These cavities provide ample active nucleation sites that contribute to the early ONB and the enhanced HTC (refer to Figure S7, Supporting Information). Second, the nanowires also serve as nanoscale pin fins that dramatically increase the effective heat transfer area for heat conduction from the microchannel surface into the liquid flow.

(2) During the vapor filling stage in the plain-surface microchannels, the coalescence of bubbles merely creates an elongated vapor thread due to insufficient vapor production from the bubble nucleation stage (Figure 5a). The vapor thread meanders through the channel, causing large oscillations in both the temperature and pressure fields as shown in Figure 4. In the nanowire-coated microchannels, the large amount of vapor bubbles generated earlier quickly coalesce into vapor slugs congesting the channel and, in the meanwhile, the vapor phase velocity increases because of the increased local vapor quality. Consequently, annular flow pattern is formed easily where the vapor phase flows through a central core and the liquid phase is confined in a thin film flowing around the channel wall (Figure 5b). Since the annular flow induces much lower flow fluctuations compared to other flow patterns such as the bubbly flow and slug flow, the formation of annular flow pattern in the nanowire-coated microchannels helps to suppress the temperature and pressure fluctuations associated with the two-phase flow instability.

(3) During the upstream flooding stage, the pressure is built up at upstream location, as the channels are congested by the vapor phase, to a certain point that it pushes the single-phase liquid to flow downstream in both the plain-surface microchannels and the nanowire-coated microchannels, which flushes away the intermittent flow patterns and causes the wall temperature and pressure drop to decrease.

To quantify the flow boiling performance, the boiling heat transfer coefficients are calculated for both the plain-surface microchannels and nanowire-coated microchannels (see Section 3 in Supporting Information). Figure 6 shows a decreasing trend in the boiling heat transfer coefficient as a function of the wall superheat, which is a typical feature for annular flow heat transfer in microchannels. It is also shown that the heat transfer coefficient is enhanced in nanowire-coated microchannels at higher mass fluxes, i.e., $G = 238, 357$, and $571 \text{ kg/m}^2 \text{s}$; however, at the lowest mass flux of $G = 119 \text{ kg/m}^2 \text{s}$, the boiling heat transfer becomes inferior to that in the plain-surface microchannels. This can be explained by the evolution of the annular flow pattern at different mass fluxes in the nanowire-coated microchannels (see detailed discussion in Section S7 of Supporting Information). Annular flow is beneficial to boiling heat transfer due to the transient heat transfer conduction and strong interfacial evaporation through the thin liquid film, however, it is also prone to local dryout if the liquid film vanishes, for instance, when there is insufficient liquid to replenish the evaporated mass, depending on the heat-to-mass flux ratio, $q/G$. At higher mass flux, the liquid film forms more easily and is able to persist through the duration of annular flow in the nanowire-coated microchannels (shown in Figure S8(b)), which results in enhanced boiling heat transfer. But at the lowest mass flux, the liquid film disappears more quickly in the two-phase flow cycle in the nanowire-coated microchannels (shown in Figure S8), and the consequent dryout leads to inferior heat transfer performance comparing to that in the plain-surface microchannels.

To examine the structural integrity, we have carefully inspected the nanowire-coated microchannels after the flow boiling test at $q = 115 \text{ W/cm}^2$ for the highest mass flux of $G = 571 \text{ kg/m}^2 \text{s}$. As shown in Figure 7, no visible morphology change can be detected for the nanowire coatings, and the microcavities remain almost identical to those before the test (as shown in Figure 2). Therefore, due to the native bounding
between the nanowires and the Si substrate, the nanowire coatings are robust and can be used to enhance the flow boiling heat transfer performance over a long period of time.

In summary, we present in this Letter the very first effort on exploiting the integration of nanowires in microchannel heat sinks for enhancing flow boiling heat transfer. A monolithic micro/nanofabrication process has been developed to fabricate silicon nanowire-coated microchannels. Flow boiling heat transfer tests were conducted in both the plain-surface microchannels and the nanowire-coated microchannels. The wall temperature and pressure drop measurements were taken over a wide range of mass flux and applied heat flux, and the two-phase flow patterns were visualized. It is found that flow boiling heat transfer is significantly enhanced in the nanowire-coated microchannels, including an early onset of nucleate boiling, a delayed onset of flow oscillation, suppressed oscillating amplitude of temperature and pressure drop, and an augmented heat transfer coefficient at moderate-to-high mass flux. The enhancement is attributed to abundant nucleation sites with a wide size range, enhanced capillary wetting facilitated by the superhydrophilic surface as well as the promoted formation of annular flow pattern. At low mass flux, the flow boiling performance is degraded in the nanowire-coated microchannels due to early dryout of the liquid film in the annular flow. Finally, the nanowire coatings are found to be very stable, due to the native bounding between the nanowires and Si substrate, and can enable long-term flow boiling performance enhancement.

**ASSOCIATED CONTENT**

**Supporting Information**

The fabrication processes of the microchannel arrays, experimental details about data reduction, contact angle
measurement, plot of active size range of nucleate boiling, and supplementary results and high-speed images. This material is available free of charge via the Internet at http://pubs.acs.org.

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Notes
The authors declare no competing financial interest.

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